Circuits Spring 2017

#1. (7 points)



a) For the above circuit find the input admittance,  $y_{in}(s)$ , seen between nodes 1 and 2. Present the results as a polynomial numerator over polynomial denominator.

b) Give the conditions for this circuit to become a sinusoidal oscillator when there is a short circuit between nodes 1 and 2.

#. (6 points)



In the above circuit the op-amp has no current into its + & - leads and has the open circuit complex frequency gain (for which K and  $\sigma$  are positive numbers)

$$\frac{V_o}{V_d}(s) = \frac{K}{s+\sigma}$$

a) Give the circuit gain

$$\frac{V_o}{V_i}(s)$$

b) What type of filter is this?

#3. (7 points)

For the following diode circuit assume the diode, Dv, is described by the voltage V controlled by the current I via the cubic law:

$$V = V_d + K_d[I^3 - 10^{-6}I]$$
  $V_d = 1V, K_d = 6x10^8 V/A^3$ 

In the circuit C = 20nFd.  $I_B = 2$ mA is a bias current and  $I_{in}$  is a small signal current.



- a) Sketch the diode V vs. I curve for -2mA < I < +2mA giving the local maxima and minima values of V.
- b) Add a load line passing through the point V=0 at I=I<sub>B</sub>, the bias current point of I<sub>B</sub> =2mA, as well as the Q point, V=V<sub>Q</sub> at I=I<sub>Q</sub>=0.
- c) Find the value of the load resistance, R<sub>L</sub>, to give this load line.
- d) Find the small signal diode resistance  $r_d$  at the Q point.
- e) Give the small signal differential equation for  $V_{out}(t)$  with  $I_{in}(t)$  as forcing function.
- f) Give the small signal transfer function.  $T(s) = \frac{V_{out}}{I_{in}}(s)$

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$$\frac{q_{L}}{q_{0}} \int_{d_{1}}^{d_{1}} = \frac{1}{R + \frac{1}{C_{R} + 0_{0} + \frac{1}{L_{R}}}} = \frac{1}{R + \frac{1}{C_{R} + 1} L_{0}^{R} + 1} = \frac{1}{RLCR^{2} + RLCRR^{2} + RLCRR^{2$$