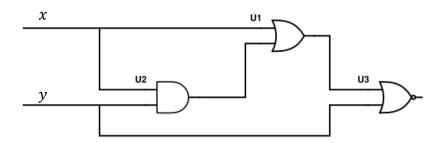
9/21/15

Don't Cares



1. Observability Don't Cares:

For which settings of x, y does the output of U1 "not matter" for the final output of the circuit?

2. Satisfiability Don't Cares:

Which setting of the input wires to U1 cannot occur?

Universal Gates

1. Prove that the 3-input gate $f(x, y, z) = x\overline{y}z + x + y\overline{z}$ is universal.