

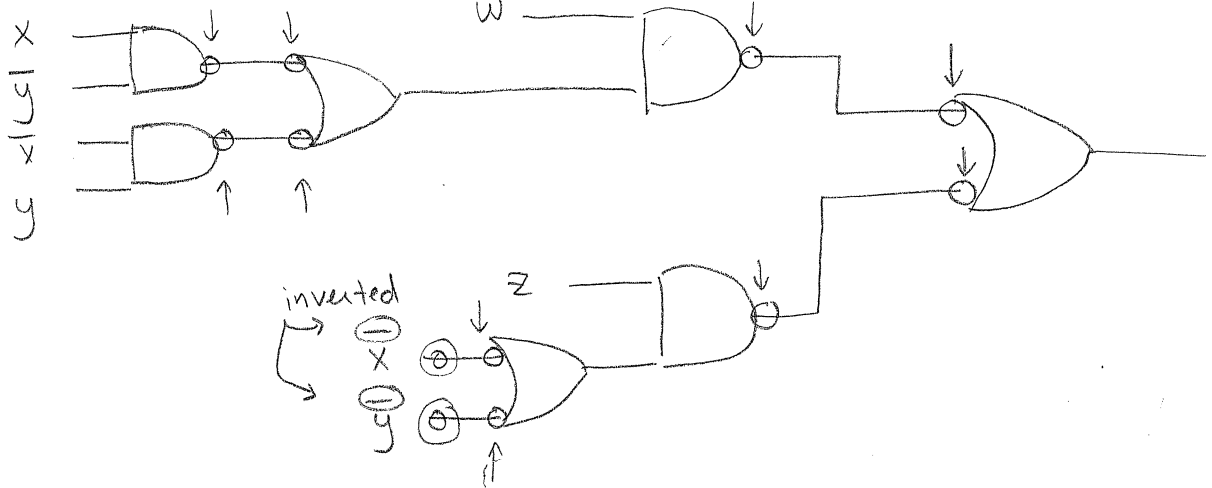
NAND Gate Realizations

9/28/15

Graphical Procedure

Convert the following Boolean expressions into logic diagrams and using the graphical procedures, convert the logic diagram into a logic diagram consisting of only nand-gates.

1. $f(w, x, y, z) = \bar{w}(x\bar{y} + \bar{x}y) + z(x + y)$



2. $f(w, x, y, z) = (\bar{x} + yz)[w + (y + z)(\bar{y} + \bar{z})]$

